



Shri Vaishnav Vidyapeeth Vishwavidyalaya
Shri Vaishnav Institute of Technology and Science
Choice Based Credit System (CBCS) Scheme in the light of NEP-2020
Diploma in Electronics

SEMESTER - V (2024-2027)

S. No.	COURSE CODE	COURSE NAME	TEACHING SCHEME/WEEK			CREDITS	EXAMINATION SCHEME					TOTAL MARKS
			L	T	P		THEORY			PRACTICAL		
							End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assessment t* (20%)	End Sem University Exam (60%)	Teachers Assessment t* (40%)	
1	DTEE502	Industrial Electronics	3	0	2	4	60	20	20	30	20	150
2	DTEE601N	Control Systems	3	1	2	5	60	20	20	30	20	150
3	DTET502	Embedded Systems	3	0	2	4	60	20	20	30	20	150
4	-	Generic Elective II	4	0	0	4	60	20	20	0	0	100
5	DTEEE501	Minor Project	0	0	4	2	0	0	0	30	20	50
6	DTEEE502	Industrial Training	0	0	2	1	0	0	0	0	50	50
TOTAL			13	1	12	20	240	80	80	120	130	650

Legends: L - Lecture ; T - Tutorial/Teacher Guided Student Activity ; P - Practical ;

*Teacher Assessment shall be based following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

Signature
22/7/26

Signature
Chairperson
Board of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Signature
Chairperson
Faculty of Studies
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Signature
Controller of Examinations
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Signature
Registrar
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore

Signature
Vice Chancellor
Shri Vaishnav Vidyapeeth
Vishwavidyalaya, Indore